

# Low temperature crystallization of $Cu_2ZnSnSe_4$ thin films using binary selenide precursors

Rhishikesh Mahadev Patil<sup>1</sup> · Dipak Ramdas Nagapure<sup>1</sup> · G. Swapna Mary<sup>1</sup> · G. Hema Chandra<sup>1</sup> · M. Anantha Sunil<sup>2</sup> · Y. P. Venkata Subbaiah<sup>3</sup> · P. Prathap<sup>4</sup> · Mukul Gupta<sup>5</sup> · R. Prasada Rao<sup>6</sup>

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**Abstract** In the present paper, a novel process for synthesis of  $Cu_2ZnSnSe_4$  thin films via low temperature selenization (350 °C) of multiple stacks of binary selenides has been reported. Further, the influence of selenization temperature (250–450 °C) on the physical properties of  $Cu_2ZnSnSe_4$  thin films was studied and discussed herein. The Rietveld refinement from X-ray diffraction data of  $Cu_2ZnSnSe_4$  films grown at a selenization temperature of 350 °C was found to be single phase with kesterite type crystal structure and having lattice parameters a = 5.695 Å, c = 11.334 Å. Raman spectra recorded using multi excitation wavelength sources under non-resonant and near resonant conditions confirms the formation of single phase  $Cu_2ZnSnSe_4$  films. Secondary ion mass spectroscopic (SIMS) analysis demonstrated that

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G. Hema Chandra drghc@rediffmail.com

- <sup>1</sup> Thin Film Laboratory, Department of Physics, Visvesvaraya National Institute of Technology, Nagpur, Maharashtra 440 010, India
- <sup>2</sup> Energy and Health Monitoring Instrumentation Laboratory, Department of Instrumentation & Applied Physics, Indian Institute of Science, Bangalore 560 012, India
- <sup>3</sup> Advanced Materials Research Laboratory, Department of Physics, Yogi Vemana University, Kadapa, Andhra Pradesh 516 003, India
- <sup>4</sup> National Physical Laboratory, K. S. Krishnan Road, New Delhi 110 012, India
- <sup>5</sup> UGC-DAE Consortium for Scientific Research, Khandwa Road, Indore 452 017, India
- <sup>6</sup> Department of Materials Science and Engineering, National University of Singapore, Singapore 117576, Singapore

composition of elements across the thickness is fairly uniform. Energy dispersive X-ray analysis measurement reveals that the obtained films are Cu-poor and Zn-rich. The scanning electron micrographs of binary selenide stacks selenized at a temperature of 350 °C shows randomly oriented cylindrical grains. The optical absorption studies indicated a direct band gap of 1.01 eV. The films showed *p*-type conductivity with electrical resistivity of 4.66  $\Omega$  cm, Hall mobility of 15.17 cm<sup>2</sup> (Vs)<sup>-1</sup> and carrier concentration of 8.82×10<sup>16</sup> cm<sup>-3</sup>.

## **1** Introduction

Photovoltaic's has emerged as an important area of energy harvesting research due to its high abundance and sustainability. Among available photovoltaic technologies, the thin film photovoltaic route found to be viable and competing with conventional technologies. To date, the most leading and promising absorber material for thin film solar cells is Cu(In,Ga)Se<sub>2</sub> (CIGS) with a conversion efficiency of 22.6% [1]. However, the cost of CIGS photovoltaic devices is still high due to utilization of non-abundant In and Ga. To compensate such limitations it is required to replace the trending CIGS with an absorber material constituted of elements that are earth abundant and possessing suitable photovoltaic properties. This led the researchers focusing aim towards earth abundant absorbers like Cu<sub>2</sub>ZnSnSe<sub>4</sub> (CZTSe),  $Cu_2ZnSnS_4$  and  $Cu_2ZnSn(S_xSe_{1-x})_4$  for thin films solar cells. Among them, Cu<sub>2</sub>ZnSnSe<sub>4</sub> material has a direct optical band gap~1 eV with high absorption coefficient  $(>10^4 \text{ cm}^{-1})$  and achieved a photovoltaic conversion efficiency of 11.6% [2] till date. As this achieved efficiency is still far away from theoretical efficiency of CZTSe and incomparable with CIGS [3], a detailed study on  $Cu_2ZnSnSe_4$  material is required to improve its photovoltaic performance.

Two stage process found to be preferable and very successful for growth of  $Cu_2ZnSnSe_4$  films [4] due to its scalability with large throughput. In first step, metallic stacks of Cu–Zn–Sn were deposited onto glass/Mo coated glass substrate using several techniques such as co-evaporation [5], sputtering [6–12], vacuum evaporation [13], electro deposition [14–16], e-beam evaporation [17], spin coating [18] and knife edge coating [19]. Later, selenization of metallic precursors was carried out using elemental selenium/H<sub>2</sub>Se gas in presence of inert Ar/N<sub>2</sub> atmosphere at elevated temperature ( $\geq$ 450 °C).

Although, high quality  $Cu_2ZnSnSe_4$  thin films were prepared using selenization of metallic precursors at elevated temperature ( $\geq 450$  °C), but found to contain minor secondary phases, which are affecting device performance adversely [20]. Also, poor adhesion with molybdenum coated substrates was found to be a major drawback with pure metal precursors due to the large expansion during selenization. Further, it is observed that incorporation of Se into Mo back contact increased with increase in selenization temperature ( $\geq 450$  °C) causing transformation of Mo into MoSe<sub>2</sub> leading to delamination of the films, which is further detrimental to device parameters such as open circuit voltage and fill factor. Additionally, the highly resistive nature of MoSe<sub>2</sub> [20, 21] results in poor device performance.

To overcome the aforementioned problems associated with selenization of metal precursors at elevated temperatures, it is necessary to focus on low temperature annealing treatment utilizing precursors of metal selenides. An improvement in the adhesion was observed with the inclusion of binary selenides for growth of CuInSe<sub>2</sub> films [22, 23]. Since the densities of CuSe, ZnSe and SnSe are closer to Cu<sub>2</sub>ZnSnSe<sub>4</sub>, the expansion causes minor stress during the course of annealing. Although, few reports are available on preparation of CZTSe thin films using pure binary selenide [19, 24–27] precursors, but includes high temperature selenization process that too focused particularly on Glass/ZnSe/SnSe(SnSe<sub>2</sub>)/CuSe(Cu<sub>2-x</sub>Se) stacking order. As CZTSe crystallization is mainly dependent on the stacking order of precursor and further stack order is important in obtaining favorable optoelectronic properties for photovoltaic applications [28], the present investigation is aimed at utilizing a novel stacking order (SnSe/Se/CuSe/Se/ZnSe/Se/CuSe/Se) that enables low temperature selenization for realizing Cu<sub>2</sub>ZnSnSe<sub>4</sub> thin films suitable for fabricating thin film solar cells on flexible substrates for role to role device application.

#### 2 Experimental

Cu<sub>2</sub>ZnSnSe<sub>4</sub> thin films were prepared using a two stage process. In the first step, deposition of multilayer precursor stack using binary selenides was carried out in high vacuum. Later, these multilayer precursor stacks were annealed in selenium atmosphere at a pressure of 100 mbar. The stacks of CuSe, ZnSe, SnSe and elemental Se (obtained from Alfa Aesar, USA) were deposited onto the ultrasonically cleaned soda lime glass substrate held at substrate temperature of 100 °C using e-beam evaporation method. A pressure of  $1 \times 10^{-5}$  mbar was maintained during deposition of precursor layers [SnSe/Se/CuSe/Se/ZnSe/Se/CuSe/Se] × 4. The thickness of each precursor layer was chosen in such way to acquire Cu-poor and Zn-rich composition. The thickness of the precursor layers during deposition was monitored using quartz crystal thickness monitor (Model: CTM-200) and the total thickness of precursor film was found to be 580 nm.

Precursor layers were selenized inside partially opened graphite box of volume 13 cm<sup>3</sup> with conventional thermal process using horizontal tabular furnace. The graphite box carrying precursor films and 50 mg of selenium powder was inserted into quartz tube. This graphite box was partially opened to maintain the selenium atmosphere inside it and to enable the reaction with the precursor in an inert atmosphere [10]. Initially, quartz tube was evacuated using rotary pump to a pressure of  $10^{-2}$  mbar. Later, the temperature was increased to 170 °C, at which Ar gas was purged into quartz tube to maintain a pressure of 100 mbar. Further, the temperature was increased to 200 °C and pre-annealing treatment was conducted at this temperature for 30 min to ensure the mixing of selenium into the alloy precursor. Later, the temperature was increased to annealing temperature ranging from 250 to 450 °C and maintained at that temperature for 30 min to re-crystallize the pre-annealed film and then it was allowed to cool naturally to room temperature. Schematic diagram of preparation of Cu<sub>2</sub>ZnSnSe<sub>4</sub> thin films using the two stage approach is shown in supplementary data.

The thickness of selenized CZTSe films were measured using Dektak XT Surface profiler (Bruker) with 12.5  $\mu$ m stylus radius. The surface morphology of selenized films was examined using ULTRA55 FE-SEM Carl Zeiss MonoKl. Energy dispersive analysis of X-ray (EDAX) attached to ULTRA55 FE-SEM was used to measure the chemical composition of selenized films with an acceleration potential of 25 kV. The energy dispersive X-ray detector (EDX) was calibrated by single crystal silicon with a measurement error of ±1 at.%. Hiden Analytical SIMS work station operated in ultrahigh vacuum with oxygen ion (Energy: 5 keV) source was used to analyze the elemental depth profile of CZTSe thin films. A BRUKER D8 advance X-ray diffractometer (XRD) with a Cu-K<sub>\alpha</sub> radiation source ( $\lambda$  = 1.5406 Å) was used to determine the structural properties of selenized CZTSe thin films with 20 range from 15° to 60° with a scan rate of 0.02°/second. General Structure Analysis System (GSAS) equipped with graphical user interface [29] was used for Rietveld refinement of XRD pattern. Raman spectra was recorded using Jobin Yvan LabRAM HR in backscattering configuration with different excitation laser source of wavelengths 455, 514 and 785 nm. Spectral transmittance and reflectance of the films was recorded in the wavelength range 400–2000 nm in steps of 2 nm using JASCO V-670 UV-Vis-NIR double beam spectrophotometer. Electrical properties of the films at room temperature were determined using a Hall effect measurement (ECOPIA HMS-3000) with an applied magnetic field of 1 T and a current of 0.1 mA.

## 3 Results and discussion

#### 3.1 Compositional analysis

The chemical composition of the multiple stacks selenized at different temperatures were quantitatively measured using energy dispersive X-ray spectroscopy. The measurements were carried out at different locations for each film and are found to be homogeneous throughout the film and their mean values are presented in Table 1. It is observed that, the composition of Cu<sub>2</sub>ZnSnSe<sub>4</sub> thin films significantly depends on selenization temperature. With an increase in selenization temperature of films from 250 °C to 450 °C, the Zn/Sn, Cu/(Zn + Sn) and Se/(Cu + Zn + Sn) ratios are found to vary between 0.97 and 6.07, 0.7–0.85 and 1.20–0.76, respectively. The multiple stacks selenized below 350 °C have shown Cu deficiency and excess Sn and Se in the composition. The composition of multiple stacks selenized at a temperature of 350 °C was found to be slightly Cu-poor and Zn-rich and the estimated atomic percentages for copper, zinc, tin and selenium were 21.82, 14.03, 12.31 and 51.84, respectively. While the films selenized above 350 °C shows significant loss of Sn and Se and is attributed to re-evaporation of SnSe<sub>x</sub> (x = 1 or 2) and Se.

The SIMS depth profile of multiple stacks selenized at a temperature of 350 °C is shown in Fig. 1. It is observed that the distribution of Cu, Sn and Se found to be homogeneous



Fig. 1 SIMS depth profiles of precursor stacks selenized at 350 °C

across the thickness of the film, whereas Zn content appeared relatively more in the top portion of the film indicating the out diffusion of Zn. Comprehensively, the distribution of all the constituent elements are found to be uniform for the multiple stacks selenized at a temperature of 350 °C.

#### 3.2 Structural analysis

The selenization temperature ( $T_{se}$ ) was found to have significant influence on the structure of the films. Figure 2 shows the XRD pattern of multiple stacks selenized at various temperatures. From XRD pattern, it is observed that all the selenized precursor stacks are polycrystalline in nature. The precursor stacks selenized at a temperature of 250 °C shows the XRD peaks at 20 values of 27.31°, 45.16° and 53.51° reveals the formation of ternary Cu<sub>2</sub>SnSe<sub>3</sub> phase (monoclinic) along with ZnSe phase (hexagonal). The pre-annealing treatment at 200 °C and subsequent selenization at 250 °C found to be favourable for reaction between CuSe and SnSe to form ternary Cu<sub>2</sub>SnSe<sub>3</sub> phase. The XRD pattern of precursor stacks selenized at a temperature of 300 °C shows the peaks at 20 values of 27.21°, 45.24° and 53.53°. The shift in dominant peak (112) towards slightly lower 20 value (27.21°) reveals

**Table 1** Elemental compositionof CZTSe thin films determinedfrom EDX analysis

 $\frac{Zn}{Sn}$  $T_{Se}$  (°C) CuSe Cu (at.%) Zn (at.%) Thickness (nm) Sn (at.%) Se (at.%)  $\overline{(Cu+Zn+Sn)}$  $\overline{(Zn+Sn)}$ 250 18.79 13.13 13.42 54.66 0.97 0.70 1.20 950 300 20.16 13.68 13.04 53.12 1.04 0.75 1.13 880 350 21.82 14.03 12.31 51.84 1.13 0.82 1.07 810 400 22.89 16.82 10.93 49.36 1.53 0.82 0.97 760 425 24.68 19.69 9.17 46.46 2.14 0.85 0.86 585 450 26.10 26.32 4.33 43.25 6.07 0.85 0.76 440



Fig. 2 XRD pattern of precursor stacks selenized at different temperatures

the initiation of  $Cu_2ZnSnSe_4$  phase formation. However, the existence of ternary  $Cu_2SnSe_3$  and binary ZnSe phase cannot be confirmed solely by XRD measurements, since the 2 $\theta$  values corresponding to  $Cu_2ZnSnSe_4$ ,  $Cu_2SnSe_3$  and ZnSe phases are very similar to each other.

The diffraction peaks observed at  $2\theta$  values of  $27.21^{\circ}$ , 45.15° and 53.41° for multiple stacks selenized at 350 °C are corresponding to (112), (204) and (312) planes of tetragonal Cu<sub>2</sub>ZnSnSe<sub>4</sub> (JCPDS # 52-0868). The grown Cu<sub>2</sub>ZnSnSe<sub>4</sub> thin films shows (112) plane as a strong preferred orientation and no secondary phases are observed, which is further verified by Raman spectroscopy. The formation of single phase Cu<sub>2</sub>ZnSnSe<sub>4</sub> thin films at a selenization temperature of 350 °C is attributed to thermally induced enhancement in the chemical kinetics of overall reaction, which results in complete reaction between ternary and binary selenide results in formation of quaternary Cu<sub>2</sub>ZnSnSe<sub>4</sub> compound film. Rietveld refinement was performed for the XRD patterns of multiple stacks selenized at 350 °C (shown in supplementary data) using GSAS to further confirm the structure. Rietveld refinement of the XRD pattern for the CZTSe films selenized at 350 °C shows an Rwp = 3.74 with goodness of fit  $\chi^2 = 1.022$ . The lattice parameters of Cu<sub>2</sub>ZnSnSe<sub>4</sub> found to be a = 5.695 (1) Å, c = 11.334 (4) Å with a space group of I4,

which are in accordance with the values reported in literature [29]. From XRD data ( $c/2a \sim 1$ ), diffraction peaks and lattice constants it is confirmed that the multiple stacks selenized at a temperature of 350 °C has a kesterite structure [30]. Single phase was observed for the precursor stacks selenized at a temperature of 350 °C in spite of minor deviation in composition is attributed to the self-assessing tendency of Cu<sub>2</sub>ZnSnSe<sub>4</sub> films [31]. To the best of our knowledge, none of the group has reported low temperature crystallization of Cu<sub>2</sub>ZnSnSe<sub>4</sub> thin films using stacked precursors. The texture coefficients for different crystal planes were calculated for the CZTSe film selenized at 350 °C from XRD data using the following Eq. (1)

$$TC_{(hkl)} = \frac{\frac{I_{(hkl)}}{I_{0(hkl)}}}{\frac{1}{n} \sum_{(hkl)} \left(\frac{I_{(hkl)}}{I_{0(hkl)}}\right)}$$
(1)

where TC is the texture coefficient of the corresponding (hkl) plane,  $I_{(hkl)}$  is the measured intensity of the (hkl) reflex,  $I_{o(hkl)}$  is the intensity of the same reflex in the reference diffraction pattern which is considered as a completely random oriented sample, and n is the reflection number [32]. The calculated texture coefficients of (112), (220) and (312) planes were 1.40, 0.76 and 0.83 respectively, which strongly corroborates the preferred orientation along (112) plane.

The multiple stacks selenized at the temperatures of 400 and 425 °C shows a trend similar to that of films selenized at 350 °C. However, 20 values of films grown at selenization temperature of 425 °C are slightly shifted towards higher angle is attributed to defect formation in the films due to re-evaporation of volatile elements such as Sn and Se. The selenized precursor stacks at 450 °C shows peaks at 27.35° related to (112) orientation of Cu<sub>2</sub>ZnSnSe<sub>4</sub> phase along with an additional peaks at 29.19° associated with ZnSe Phase (JCPDS # 89-2940). In addition to this, the peaks at 20 values 31.23°, 45.34° and 53.70° are observed corresponding to CuSe (hexagonal) phase (JCPDS # 34-0171). Hence, XRD pattern of precursor stacks selenized at a temperature of 450 °C reveals the decomposition of quaternary Cu<sub>2</sub>ZnSnSe<sub>4</sub> phase into CuSe and ZnSe phases.

#### 3.3 Raman spectroscopy

It is very difficult to confirm the formation of single phase  $Cu_2ZnSnSe_4$  solely from XRD analysis, since the characteristic XRD peaks of  $Cu_2ZnSnSe_4$  phase overlaps with the peaks of  $Cu_2SnSe_3$  and ZnSe. Raman spectroscopy was used as a complementary tool to distinguish and ensure the formation of  $Cu_2ZnSnSe_4$  phase. Raman spectra obtained using an excitation wavelength of 514 nm for multiple stacks selenized at different temperatures are shown in Fig. 3a. The



Fig. 3 a Raman spectra of the precursor stacks selenized at different temperatures. b Multi wavelength Raman spectra of CZTSe thin films selenized at 350 °C

precursor's selenized at a temperature of 250 °C shows the broad and less intense Raman peaks at 67, 183, 232 and 253 cm<sup>-1</sup>. A minor peak at 67 cm<sup>-1</sup> is associated with SnSe (orthorhombic) phase [33] and other two peaks at 183 and  $232 \text{ cm}^{-1}$  can be attributed to ternary Cu<sub>2</sub>SnSe<sub>3</sub> phase [34]. A peak at 183 cm<sup>-1</sup> relates with both Cu<sub>2</sub>SnSe<sub>3</sub> and SnSe<sub>2</sub> phase. However, no signature of SnSe<sub>2</sub> was observed in XRD measurement and hence concluded that it belongs to Cu<sub>2</sub>SnSe<sub>3</sub> and the formation of SnSe<sub>2</sub> phase can be ruled out. The Raman peak located at 253 cm<sup>-1</sup> is attributed to ZnSe phase [34]. The multiple stacks selenized at a temperature of 300 °C show the Raman modes at 67, 184, 192, 234 and  $251 \text{ cm}^{-1}$ . The broad and asymmetric peaks at 67, 184 and 251 cm<sup>-1</sup> shows the presence of SnSe, Cu<sub>2</sub>SnSe<sub>3</sub> and ZnSe phases respectively, while the peaks at 192 and 234  $\text{cm}^{-1}$ are associated with Cu<sub>2</sub>ZnSnSe<sub>4</sub> phase. Hence, the Raman spectra reveal the selenization of precursor stacks at 300 °C is a pre-mature growth of Cu<sub>2</sub>ZnSnSe<sub>4</sub> phase formation.

The well-defined Raman spectra was observed for multiple stacks selenized at a temperature of 350 °C with peaks at 80, 172, 196, 234 and 244 cm<sup>-1</sup> that corresponds to single phase Cu<sub>2</sub>ZnSnSe<sub>4</sub>. The two Raman modes at 172 cm<sup>-1</sup> (A symmetry) and 196 cm<sup>-1</sup> (A symmetry) are well distinguished and symmetrical in nature. In addition to this, the peaks are observed to be sharp and intense which confirms the formation of highly crystalline and pure kesterite Cu<sub>2</sub>ZnSnSe<sub>4</sub> phase.

Similar trend was observed for Cu<sub>2</sub>ZnSnSe<sub>4</sub> thin films grown at selenization temperatures 400 and 425 °C, however the peaks are relatively broad and asymmetric in nature which is attributed to the lattice defect induced phonon confinement, low crystallinity, presence of strain and local composition deviation in the films [35]. Furthermore, both the films show an additional Raman mode at  $\sim 250 \text{ cm}^{-1} \text{ cor-}$ responding to ZnSe phase, which is due to re-evaporation of volatile SnSe phase at elevated temperatures resulted in accumulation of ZnSe phase at the surface [36]. The multiple stacks selenized at a temperature of 450 °C shows relatively low intense Raman modes of Cu<sub>2</sub>ZnSnSe<sub>4</sub> phase at 80, 173, 197 and 233  $\text{cm}^{-1}$  with an additional sharp peak at 261 cm<sup>-1</sup> revealing the presence of CuSe phase [34], as evident from XRD analysis. The observation of CuSe phase at selenization temperature 450 °C clearly indicates phase separation due to the re-evaporation of volatile elements as supplemented by EDX analysis. Full width at half maximum (FWHM) value corresponding to Raman peak at 196 cm<sup>-1</sup> for the film selenized at temperature 350 °C found to be narrow  $(3.20 \text{ cm}^{-1})$  compared to the films selenized at temperature 400 °C (5.31 cm<sup>-1</sup>) and 425 °C (12.99 cm<sup>-1</sup>) revealing improved crystallinity and minimal defects.

Usually, for structural studies, micro Raman spectroscopy measurements are carried out with the use of green excitation wavelength of 514 or 532 nm. The excitation with green wavelength may limit the identification of undesirable secondary phases thereby it requires the use of different excitation wavelengths leading to near resonant excitation conditions for certain secondary phases which enables the detection of weak modes due to enhancement in the peak intensity [37]. There is a possibility that the secondary phases like SnSe<sub>2</sub> and ZnSe may remain undetected despite the usual green Raman and XRD analysis has shown single Cu<sub>2</sub>ZnSnSe<sub>4</sub> phase for the film selenized at 350 °C. To ensure the phase purity concluded by XRD analysis and Raman spectroscopy with an excitation wavelength of 514 nm, multiple wavelength Raman measurements were carried out for the precursor stacks selenized at 350 °C. The Raman spectra observed with multi-excitation wavelengths (455, 514 and 785 nm) are depicted in Fig. 3b. Raman spectra shows peaks corresponding to Cu<sub>2</sub>ZnSnSe<sub>4</sub> phase only for all the three excitation wavelengths with no traces of secondary phases which corroborates the phase purity of Cu<sub>2</sub>ZnSnSe<sub>4</sub> thin films selenized at 350 °C.

### 3.4 Reaction mechanism

It is observed that the pre-annealing treatment, novel stacking order and thickness of each layer has enabled the formation of single phase  $Cu_2ZnSnSe_4$  at lower selenization temperature. Based on XRD and Raman analysis the reaction paths for formation of binary, ternary and quaternary phases at different selenization temperature were presented.

(1) It is well known that, CuSe react more quickly with SnSe than other copper selenides phases (CuSe<sub>2</sub>, Cu<sub>2-x</sub>Se). At lower selenization temperature ( $T_{Se}=250$  °C), CuSe reacts with SnSe and form ternary Cu<sub>2</sub>SnSe<sub>3</sub> phase. Hence, the presence of Cu<sub>2</sub>SnSe<sub>3</sub>, SnSe and ZnSe are detected.

 $2\text{CuSe} + 2\text{SnSe} + \text{ZnSe} \rightarrow \text{Cu}_2\text{SnSe}_3 + \text{SnSe} + \text{ZnSe}$  (2)

(2) Cu<sub>2</sub>SnSe<sub>3</sub> reacts with ZnSe to form Cu<sub>2</sub>ZnSnSe<sub>4</sub> phase at the selenization temperature (T<sub>Se</sub>) of 300 °C. But it is observed that, selenization temperature was insufficient to complete formation of single phase Cu<sub>2</sub>ZnSnSe<sub>4</sub>. An incomplete conversion of binaries into ternary/quaternary phases were observed due to the deficiency of CuSe.

$$2CuSe+2SnSe+ZnSe \rightarrow Cu_2SnSe_3+SnSe+ZnSe$$
 (3)

 $2Cu_2SnSe_3+SnSe+2ZnSe \rightarrow Cu_2ZnSnSe_4+Cu_2SnSe_3+SnSe+ZnSe$ (4)

(3) At  $T_{Se} = 350$  °C,  $Cu_2SnSe_3$  and ZnSe were found to react completely with each other and resulted in single phase  $Cu_2ZnSnSe_4$ .

$$2CuSe+SnSe+ZnSe \rightarrow Cu_2SnSe_3+ZnSe$$
 (5)

$$Cu_2SnSe_3 + ZnSe \to Cu_2ZnSnSe_4$$
(6)

(4) At higher selenization temperature ( $T_{Se} = 450 \text{ °C}$ ), due to the re-evaporation of SnSe decomposition of Cu<sub>2</sub>ZnSnSe<sub>4</sub> phase take place, resulting into binary CuSe and ZnSe phase along with Cu<sub>2</sub>ZnSnSe<sub>4</sub> phase.

$$2Cu_2ZnSnSe_4 \rightarrow Cu_2ZnSnSe_4 + 2CuSe + ZnSe + SnSe(g)$$
(7)

The quasi-ternary phase diagram of the CuSe–ZnSe–SnSe system is shown in Fig. 4, which depict nearly-stoichiometric composition for single-phase  $Cu_2ZnSnSe_4$  measured using EDX (green spot). The stoichiometric composition



for single-phase  $Cu_2ZnSnSe_4$  was shown by red colour in the phase diagram. From phase diagram, it is observed that CuSe-ZnSe-SnSe system exhibits very small region (blue spot) of stability for single-phase kesterite  $Cu_2ZnSnSe_4$ thin films. Furthermore, the process of formation of singlephase  $Cu_2ZnSnSe_4$  is self limiting and small deviation in composition results in the formation of various binary and ternary phases. Hence, it is required to control composition systematically.

### 3.5 Surface morphology

The FESEM micrographs of multiple stacks selenized at temperatures of 250, 350, 425 and 450 °C are shown in Fig. 5. The FESEM image of multiple stacks selenized at a temperature of 250 °C shows small sized cylindrical grains (length ~ 200 nm), which are uniformly distributed throughout the surface of substrate. With an increase in selenization temperature of Cu<sub>2</sub>ZnSnSe<sub>4</sub> thin films from 250 to 350 °C, the small sized cylindrical grains grown bigger. The grains have clear cylindrical shape of length ~ 425 nm. In addition to this, white granules of very small size (few nanometers) are anchored on the surface of the Cu<sub>2</sub>ZnSnSe<sub>4</sub> thin films. To confirm the composition of tiny granules, the elemental mapping was carried out using EDX for multiple stacks selenized at a temperature of 350 °C and results are shown in supplementary data. The elemental mapping showing a uniform distribution of constituent elements on the surface with no colour contrast that rules out the segregation of spurious binary phases on to surface. The deformation was clearly observed at the edges of grains from the micrographs of multiple stacks selenized at a temperature of 425 °C indicating an increase in the defect density arise due to decomposition of compound. Furthermore, the agglomeration of grains has been observed for stacked layers selenized at a temperature of 450 °C and large voids are present in-between the grains, which may be due to the re-evaporation of volatile elements at elevated temperature results in decomposition of quaternary Cu<sub>2</sub>ZnSnSe<sub>4</sub> phase into secondary phases. The cross sectional SEM images of the stacked layers selenized at temperature 250, 350 and 425 °C are shown in supplementary data. The cross sectional SEM of Cu<sub>2</sub>ZnSnSe<sub>4</sub> films selenized at 250 °C shows loosely packed grains with cauliflower like morphology. Whereas, the films selenized at a temperature of 350 °C show compact and larger grains. A further increase in selenization temperature to 425 °C led to a sign of slight delamination between the multi-layers structures.



Fig. 5 FESEM images of the stacked layers selenized at 250, 350, 425 and 450 °C

#### 3.6 Optical properties

Figure 6a and b shows the optical transmittance and reflectance spectra of multiple stacks selenized at different temperatures from 250 to 450 °C. The transmittance spectra of selenized stacked layers reveal that there are considerable deviations in transmittance spectra close to the absorption edge with an increase in selenization temperature. The stacked layers selenized at 250 °C shows low spectral transmittance of 15%, while transmittance of 25-50% was observed nearer to absorption edges of the films selenized at temperatures 300, 350 and 400 °C. An oscillatory behavior observed at ~1600 nm for stacked layers selenized at 300, 350 and 400 °C were caused by interference due the difference in refractive index of Cu<sub>2</sub>ZnSnSe<sub>4</sub> film and the glass substrate. An enhancement in the optical transmittance for multiple binary selenide stacks selenized at 300, 350 and 400 °C indicating a sign of an increase in grain size with less grain boundaries results in decreasing scattering of photons



**Fig. 6 a** The optical transmittance spectra of precursor layers selenized at different temperatures. **b** The optical reflectance spectra of precursor layers selenized at different temperatures

[38]. Similarly, the sharpness observed in the interference fringes of stacked layers selenized at 300, 350 and 400 °C indicates a decrease of surface roughness with improvement in the film crystallinity [39]. A low transmittance (less than 10%) was observed for stacked layers selenized at a temperature of 450 °C due to the poor crystallinity [40]. The appearance of maxima and minima in reflectance spectra indicates the specular nature of selenized thin films, thus revealing negligible possibility of scattering [41]. The optical absorption coefficient ( $\alpha$ ) is calculated using spectral transmittance (T) and reflectance (R) data with the help of Eq. 8 [34].

$$\alpha = -\frac{1}{t} ln \left[ \frac{-(1-R)^2 + \sqrt{(1-R)^4 + 4T^2R^2}}{2TR^2} \right]$$
(8)

where 't' is the thickness of the film. The nature of the optical transition and the optical band gap for each film is obtained from Eq. 9,

$$\alpha h\nu = A(h\nu - Eg)^n \tag{9}$$

where A is constant. The values of ' $\alpha$ ' are found to obey Eq. (9) for n = 1/2, suggesting that the optical transitions are direct-allowed. The optical band gap was determined by extrapolating the linear region of the plot  $(\alpha h\nu)^2$  versus h $\nu$  and taking the intercept at  $\alpha = 0$ .

Figure 7a show the plots of  $(\alpha h\nu)^2$  versus  $h\nu$  for the multiple stacks selenized at temperatures 250, 350 °C and Fig. 7b shows the plot of film selenized at 450 °C. It can be seen from Fig. 7a that the energy gap of stacked layers selenized at a temperature of 250 °C was found to be 0.81 eV, thereby confirms the formation of ternary phase (Cu<sub>2</sub>SnSe<sub>3</sub>). Even though signatures of ZnSe phase were observed from XRD and Raman analysis, no traces of it (ZnSe phase) were detected from transmittance spectrum and this is attributed to limitations associated with low spectral intensity as the fundamental edge falls below 600 nm. The optical band gap of 1.01 eV was observed for precursor stacks selenized at a temperature 350 °C indicating the formation of single phase Cu<sub>2</sub>ZnSnSe<sub>4</sub> as evident from XRD and Raman analysis. Also, it is in good agreement with reported literature [9, 11]. The Cu<sub>2</sub>ZnSnSe<sub>4</sub> thin film selenized at temperature 450 °C shows higher band gap of 1.85 eV might be due to the presence of secondary CuSe phase [42] in the film that has reflected in XRD and Raman spectra.

#### 3.7 Electrical properties

The effect of selenization temperatures on the electrical properties of  $Cu_2ZnSnSe_4$  thin films was studied using Hall Effect set up at room temperature. Despite of variation in selenization temperature, all films shows *p*-type conductivity indicating the presence of intrinsic point defects in the grown films. The electrical properties of multiple stacks



Fig. 7 a Plot of  $(\alpha h\nu)^2$  versus  $h\nu$  for precursor stacks selenized at 250 and 350 °C. b Plot of  $(\alpha h\nu)^2$  versus  $h\nu$  for precursor stacks selenized at 450 °C

 Table 2
 Electrical properties of stacked layers selenized at different temperatures

Selenization temperature (°C)	Resistivity $(\Omega \text{ cm})$	Mobility (cm <sup>2</sup> (Vs) <sup>-1</sup> )	Carrier concentration (cm <sup>-3</sup> )
250	$1.78 \times 10^{-1}$	$4.96 \times 10^{-1}$	$7.02 \times 10^{19}$
300	1.25	3.04	$1.63 \times 10^{18}$
350	4.66	15.17	$8.82 \times 10^{16}$
400	$2.60 \times 10^{-1}$	$3.41 \times 10^{-1}$	$7.03 \times 10^{19}$
425	$1.41 \times 10^{-2}$	$5.11 \times 10^{-2}$	$8.62 \times 10^{21}$
450	$8.55 \times 10^{-2}$	$1.32 \times 10^{-2}$	$5.49 \times 10^{21}$

selenized at various temperatures are shown in Table 2. The multiple stacks selenized at a temperature of 250 °C shows low resistivity due to the presence of ternary Cu<sub>2</sub>SnSe<sub>3</sub> phase. Further increase in selenization temperature from 250 to 350 °C has resulted in improvement of Hall mobility from  $4.96 \times 10^{-1}$  to 15.17 cm<sup>2</sup> (Vs)<sup>-1</sup>. The increase in selenization temperature led to reduce the defects and improve the phase purity and uniformity in composition of the Cu<sub>2</sub>ZnSnSe<sub>4</sub> films causing enhancement in the mobility. It is observed from FESEM image that with an increase in selenization temperature from 250 to 350 °C the grain sizes are also found to be increased which has reduced the grain boundary scattering of carriers thereby enhanced the hole mobility. The values of electrical resistivity, mobility and carrier concentration for Cu<sub>2</sub>ZnSnSe<sub>4</sub> films selenized at temperature 350 °C were found to be 4.66  $\Omega$  cm, 15.17 cm<sup>2</sup>(Vs)<sup>-1</sup> and  $8.82 \times 10^{16}$  cm<sup>-3</sup> respectively, which are in good agreement with reported values [31, 43]. Cu<sub>2</sub>ZnSnSe<sub>4</sub> films selenized at a temperature of 450 °C shows high carrier concentration  $5.49 \times 10^{21} \text{ cm}^{-3}$  with mobility  $1.32 \times 10^{-2} \text{ cm}^{2}(\text{Vs})^{-1}$  and resistivity  $8.55 \times 10^{-2} \Omega$  cm, which may be due to the presence of secondary CuSe phase and the decreased grain size as evident from XRD and FESEM results.

## 4 Conclusions

The high quality Cu<sub>2</sub>ZnSnSe<sub>4</sub> films were successfully prepared onto soda lime glass substrates by low temperature selenization of e-beam evaporated multiple binary selenide precursor stacks. The effect of selenization temperature on the physical properties of Cu<sub>2</sub>ZnSnSe<sub>4</sub> thin films was systematically studied. The Cu<sub>2</sub>ZnSnSe<sub>4</sub> thin films selenized at different temperatures are found polycrystalline in nature. The Cu<sub>2</sub>ZnSnSe<sub>4</sub> film grown at selenization temperature 350 °C exhibited kesterite structure having lattice parameters a = 5.695 (1) Å, c = 11.334 (4) Å with (112) preferred orientation. Raman measurement using multi-wavelength excitation confirms the grown films were single phase Cu<sub>2</sub>ZnSnSe<sub>4</sub>. The compositional analysis revealed that the obtained films are slightly Cu-poor and Zn-rich. The optical absorption studies indicated a direct band gap of 1.01 eV. The Cu<sub>2</sub>ZnSnSe<sub>4</sub> film selenized at 350 °C showed *p*-type conductivity with electrical resistivity of 4.66  $\Omega$  cm, mobility of 15.17 cm<sup>2</sup>(Vs)<sup>-1</sup> and carrier concentration of  $8.82 \times 10^{16}$  cm<sup>-3</sup>. The crystallization of Cu<sub>2</sub>ZnSnSe<sub>4</sub> thin films at low temperature (350 °C) is attributed to the utilization of novel multiple binary selenide precursor stacks followed by the pre-annealing treatment at 200 °C. The grains are observed to be relatively small in size, therefore further improvement is necessary which is subjected to execute as future work.

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#### **Compliance with ethical standards**

**Conflict of interest** The authors have no conflict of interest to declare.

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